

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. MIYAMOTO et al.
Serial No.: (New divisional application under 37 CFR §1.53[b] of U.S.
Application Serial No. 10/127,583)
Filed: January 29, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD OF MANUFACTURING THE SAME
Group: 2826
Examiner: V. MANDALA

CLAIM FOR FOREIGN PRIORITY

MS: PATENT APPLICATIONS

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

January 29, 2004

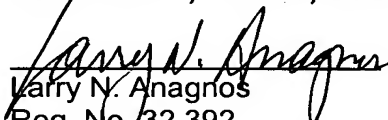
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR §1.55, applicants hereby claim
the right of priority based on the following foreign application filed in Japan:

Application No. 2001-124475, filed April 23, 2001.

A certified copy of this foreign application was filed on April 23, 2002 in
prior, parent U. S. application Serial No. 10/127,583.

Respectfully submitted,
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